

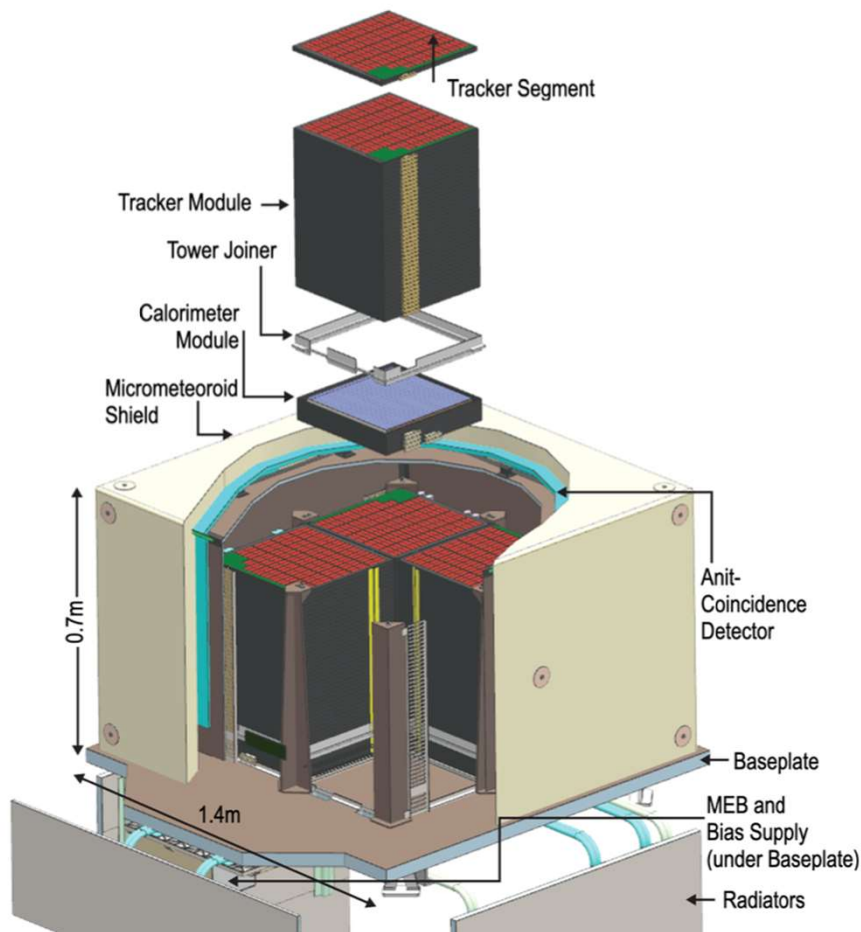
# コンプトンカメラ用HV-CMOS: AstroPixの開発現状



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- AMEGO-X (PI: R.Caputo GSFC/NASA) is a proposed explorer to study extreme astrophysical phenomena in MeV regime



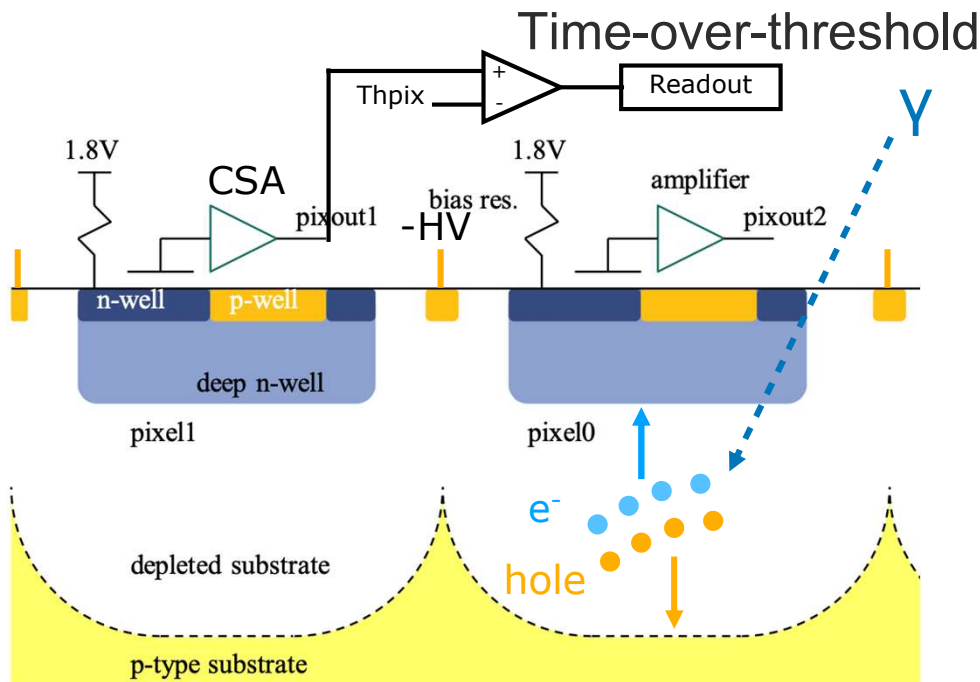
- Shed light on not-well-studied energy regime (25 keV - 1 GeV)
- Huge impact on multi-messenger astronomy thanks to its wide FoV ( $2\pi$  @  $E < 10$  MeV)
- Require a lot of low-noise pixelated silicon sensors ( $\sim 2.4 \times 10^5$  cm<sup>2</sup>) with a wide depletion layer to efficiently perform Compton reconstruction
- Power consumption of silicon sensors must be  $< 1$  mW/cm<sup>2</sup>

→ AstroPix

R. Caputo+ 2022

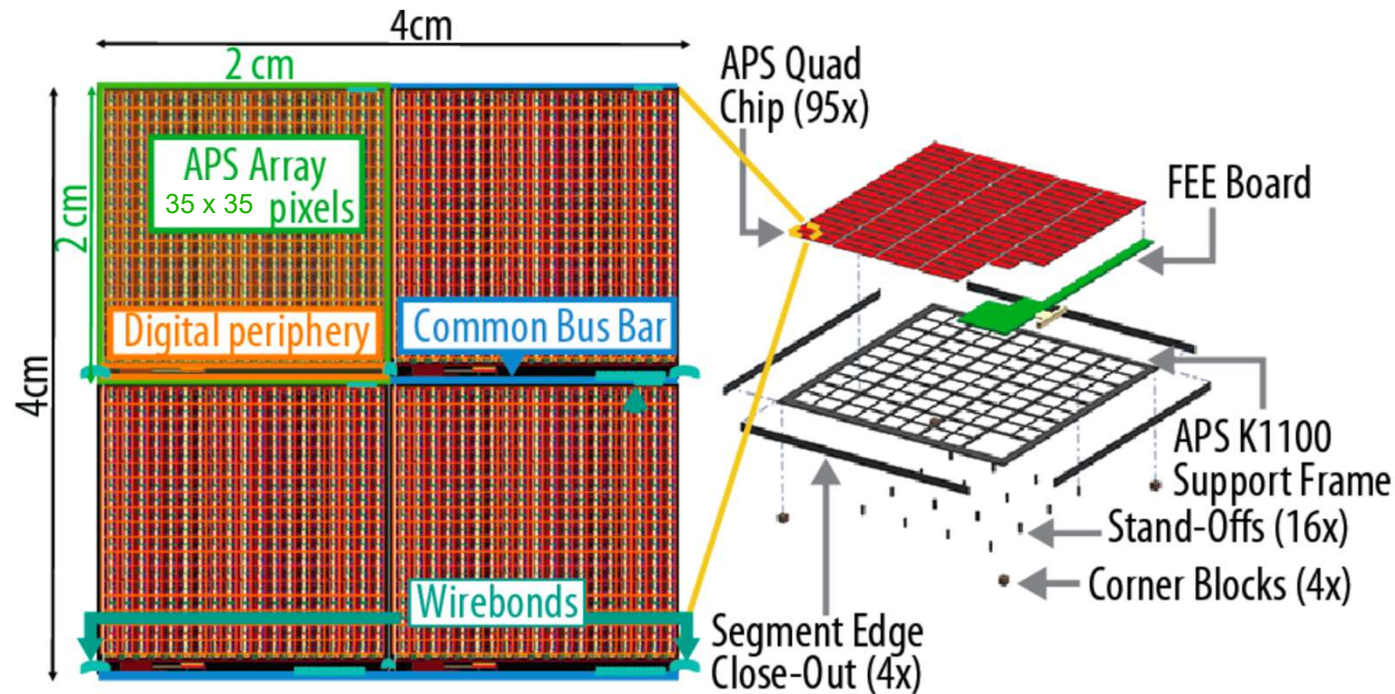
- AstroPix is a new monolithic HV-CMOS active pixel sensor
  - Full depletion achieved by applying HV
  - Signal processing (CSA  $\rightarrow$  Comparator for Time-over-threshold) is performed on pixel and digitization is done on chip
- Development based on experience from ATLASPix and MuPix

[I.Peric&N.Berger 2018](#)



- AstroPix Team
  - PI: R. Caputo (GSFC/NASA)
  - GSFC, ANL, KIT, UCSC, Hiroshima U, Nagoya U

## Quad Chip = 4 identical AstroPix array

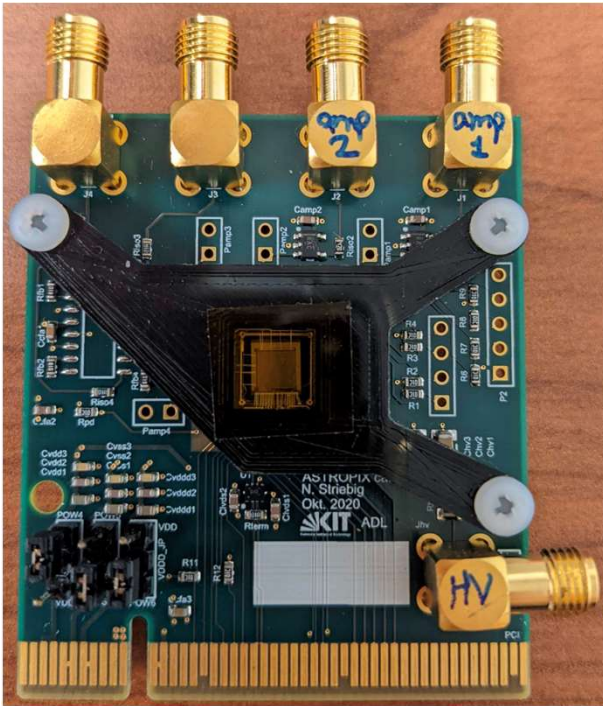


## Requirements

Pixel pitch	$500 \times 500 \mu\text{m}^2$	Dynamic range	25 keV - 700 keV
Thickness	500 $\mu\text{m}$	Energy resolution	< 10% (FWHM) at 60 keV
Power consumption	< 1 mW/cm <sup>2</sup>		

# AstroPix Series

## Version 1



0.5 x 0.5 cm<sup>2</sup> chip  
175 x 175 μm<sup>2</sup> pitch  
18 x 18 pixels  
\*14.7 mW/cm<sup>2</sup>  
\*CSA+comparator only.

## Version 2



1 x 1 cm<sup>2</sup> chip  
250 x 250 μm<sup>2</sup> pitch  
35 x 35 pixels  
\*3.4 mW/cm<sup>2</sup>

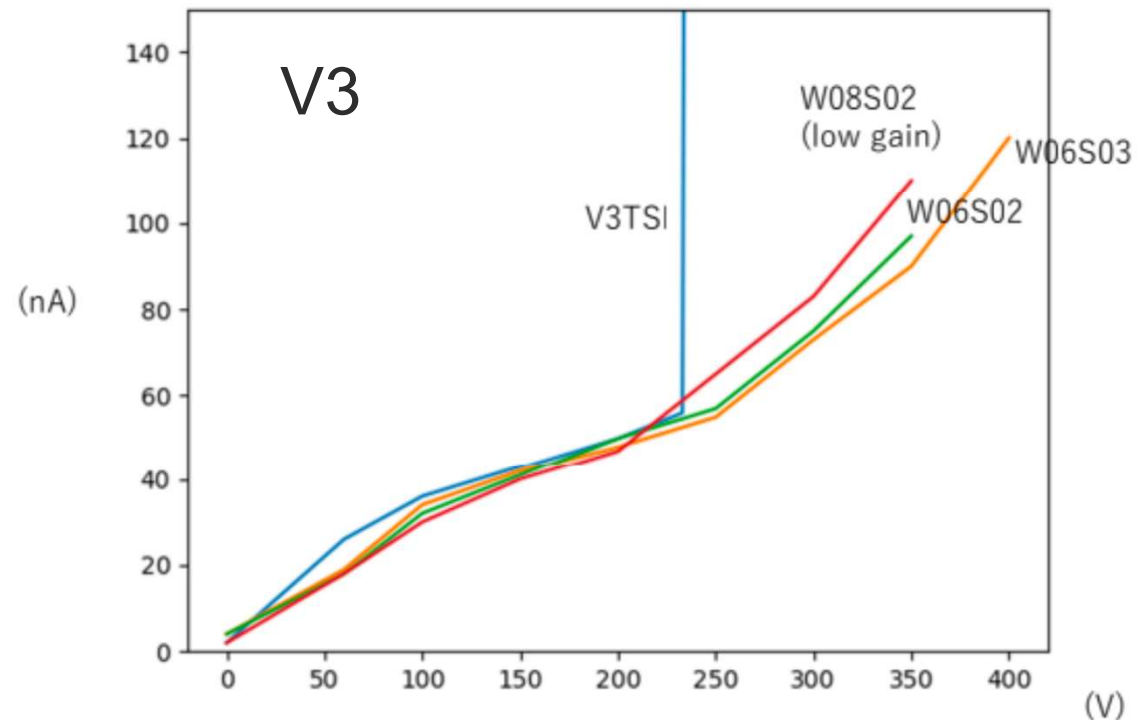
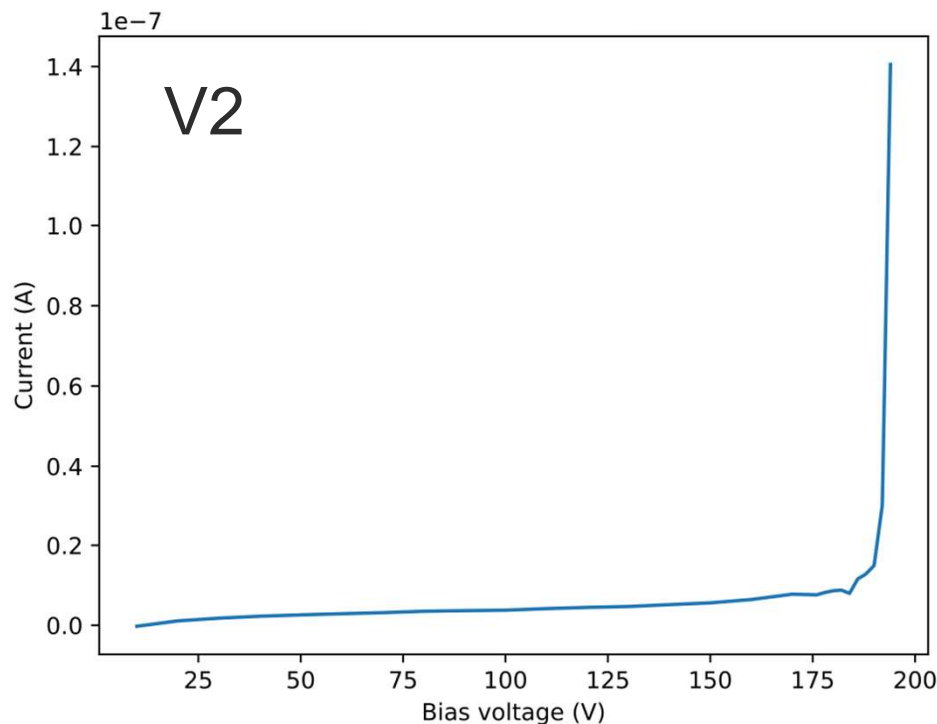
## Version 3



2 x 2 cm<sup>2</sup> chip  
500 x 500 μm<sup>2</sup> pitch  
35 x 35 pixels  
\*1.04 mW/cm<sup>2</sup>  
4.12 mW/cm<sup>2</sup> (incl. digital)

# I-V Curves

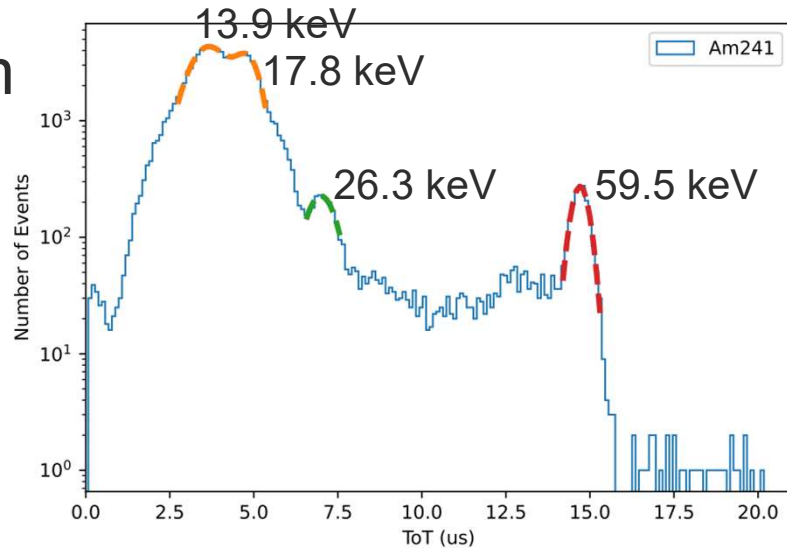
- V2: 300  $\Omega$ cm wafer chip shows breakdown at  $\sim 190$  V
- V3: 300  $\Omega$ cm wafer chip can go up to  $\sim 400$  V thanks to better clearance to chip-edge  $\leftarrow$  Results in this talk from this wafer
  - 20  $\Omega$ cm wafer chip breakdown at  $\sim 230$  V
  - 18 k $\Omega$ cm wafer chip shows high leakage current in the mA range



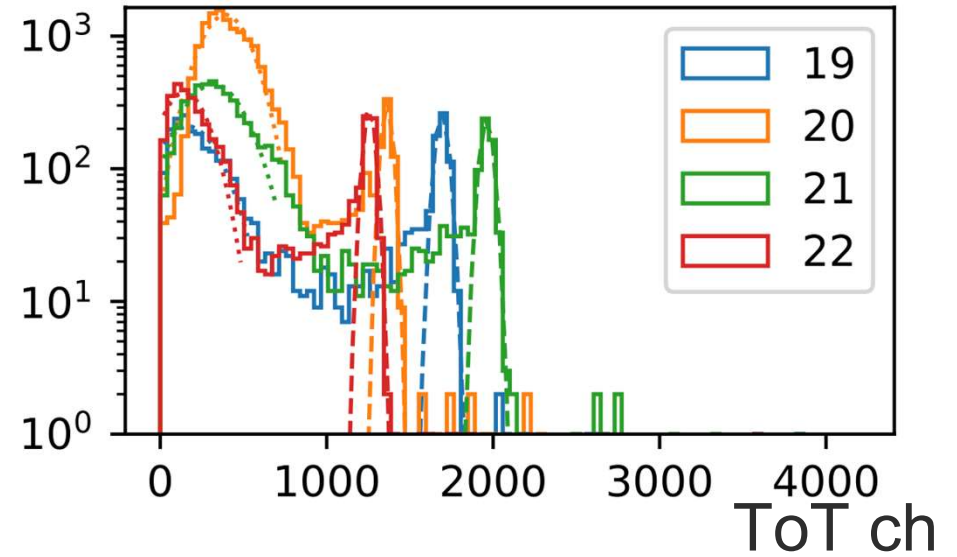
# Energy Spectra

V2

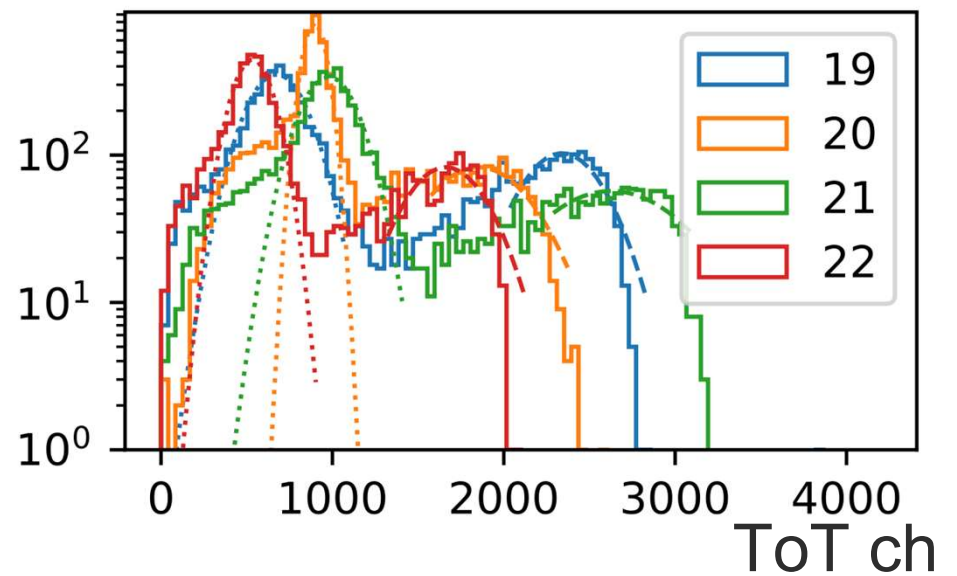
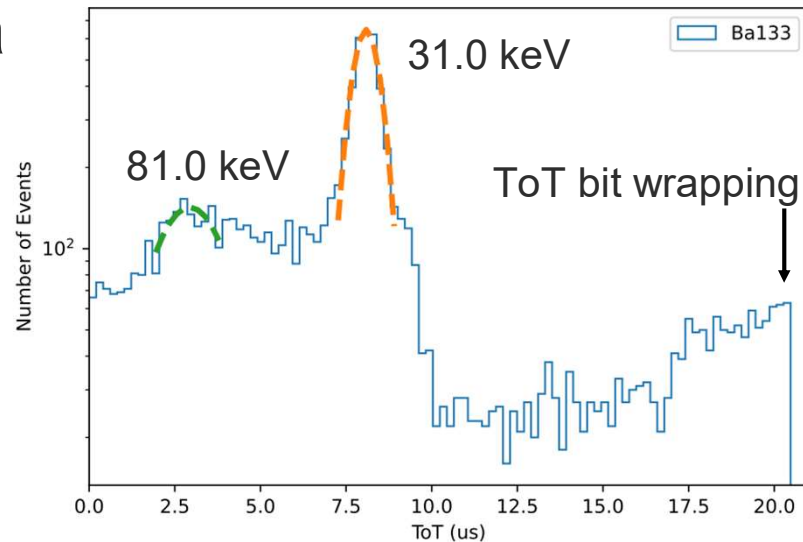
$^{241}\text{Am}$



V3



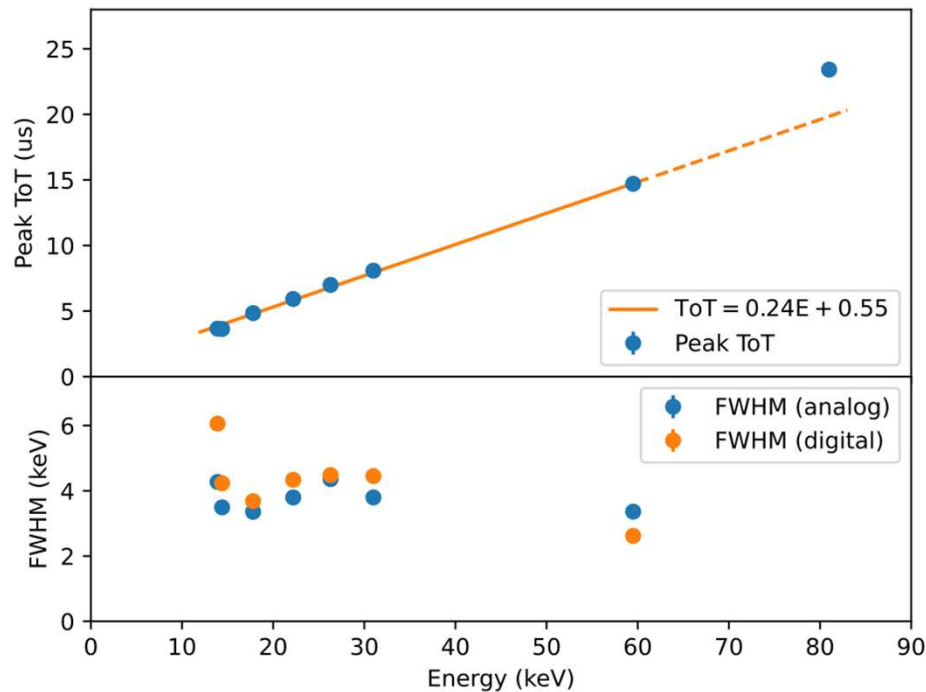
$^{133}\text{Ba}$



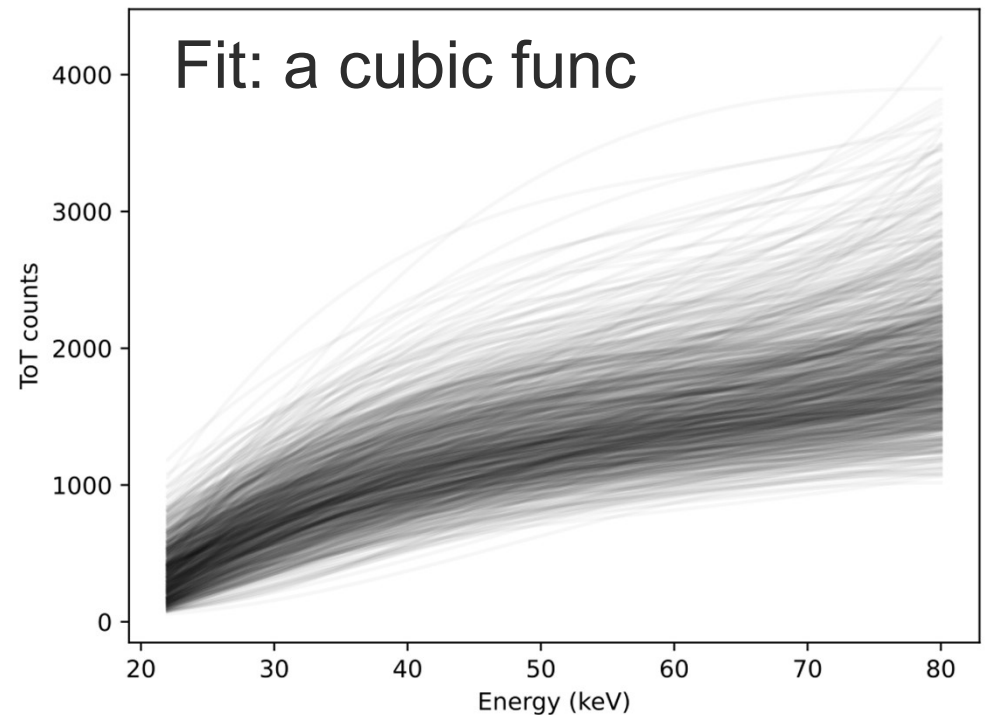
# Energy Calibration

- With updated readout FW/SWs for V3, energy calibration can be performed over the full sensor at once
- Dynamic range: 14 - 80 keV for one pixel of V2 chip  
22 - 80 keV for V3 chip

V2



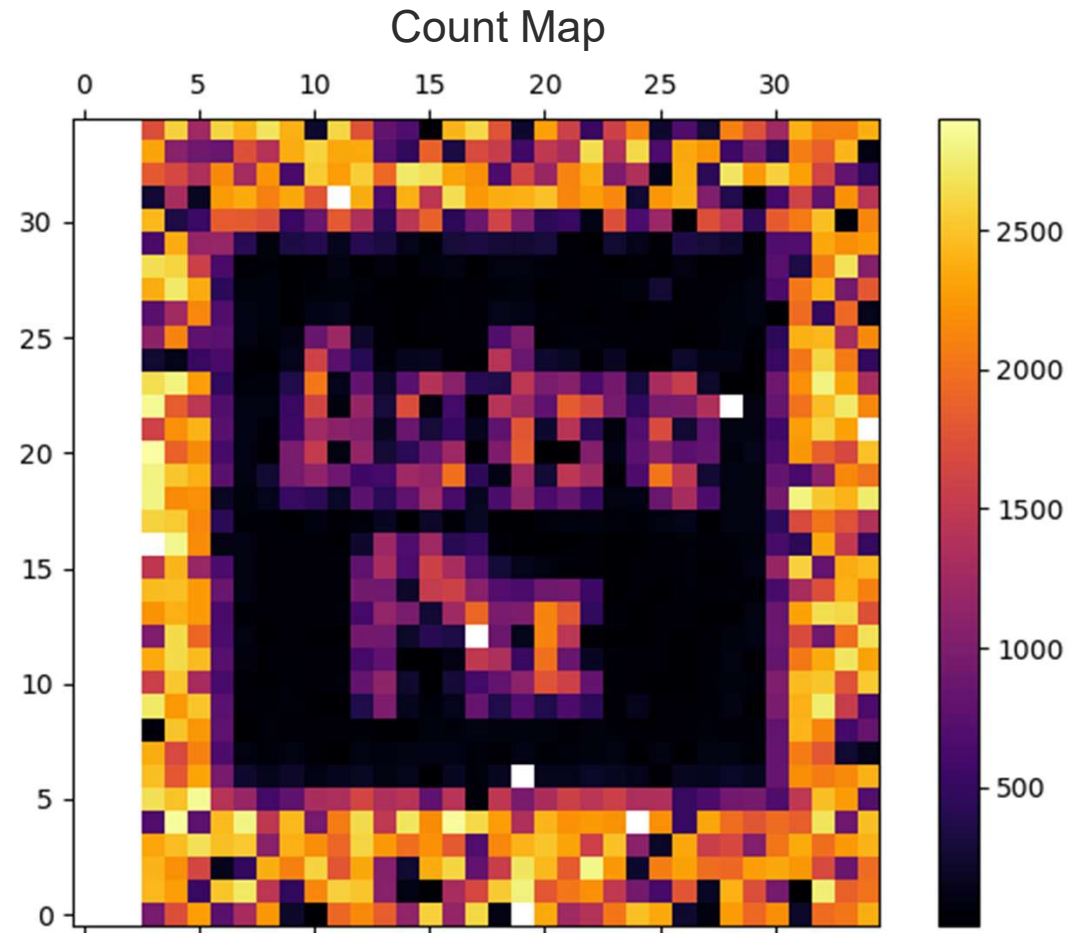
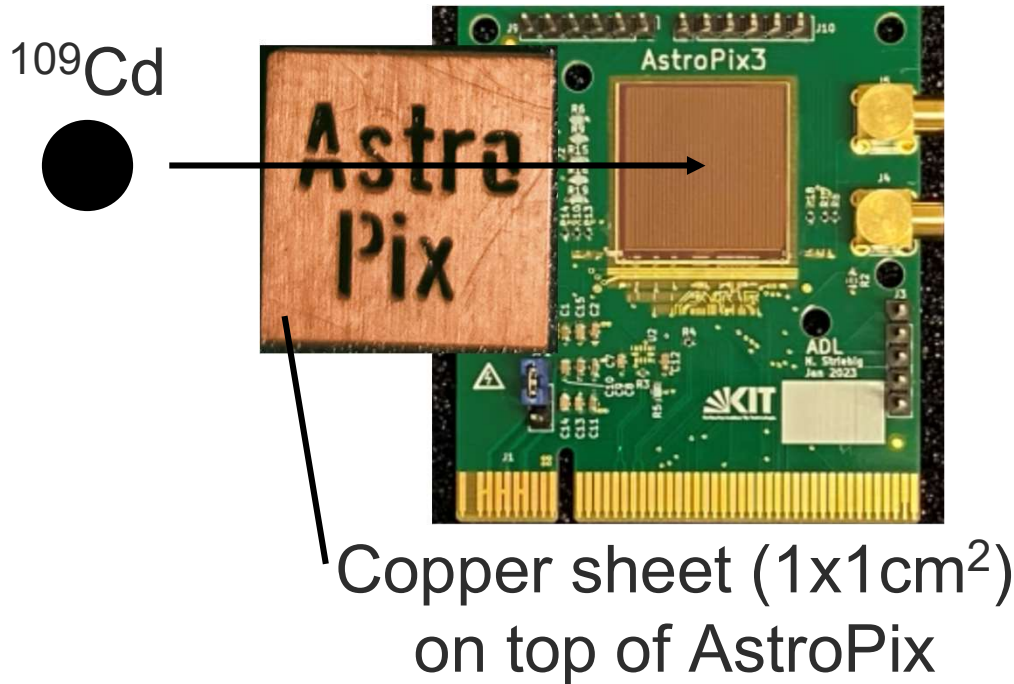
V3





# AstroPix V3: Imaging

- First picture taken by AstroPix
- Only 0.7% of pixels are noisy (masked)



# V3: Depletion Depth Measurements

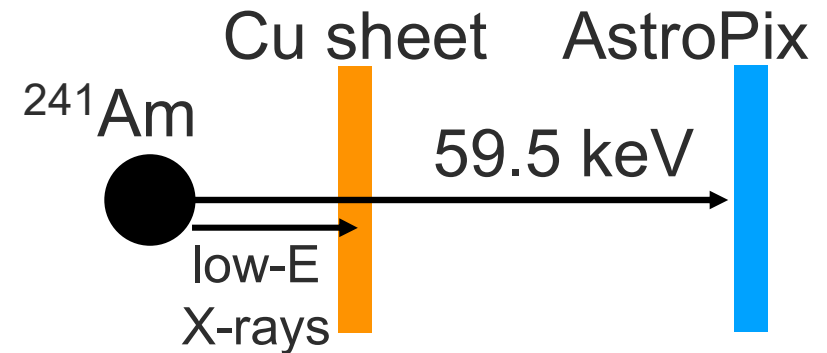
- Estimate from the detection rate of  $^{241}\text{Am}$  59.5 keV events
  - Extract photopeak events from the fitted spectrum
- Compare with a simple model

$$d = \sqrt{2\epsilon\mu\rho(V_{\text{bias}} + V_{\text{built-in}})}$$

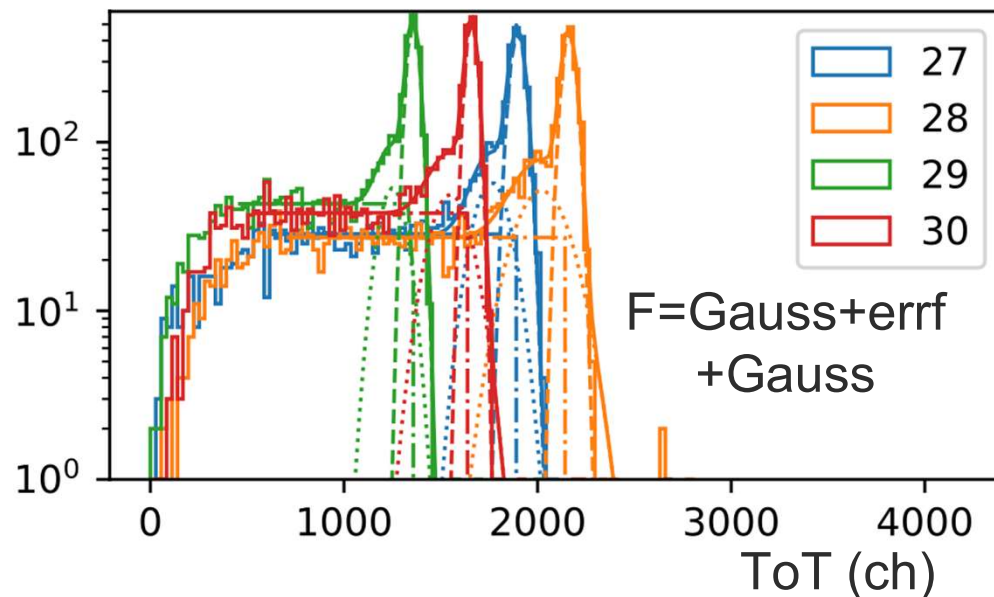
$\epsilon$  : Permittivity       $\mu$  : Hole mobility

$\rho$  : Resistivity 200-400 Ohm cm

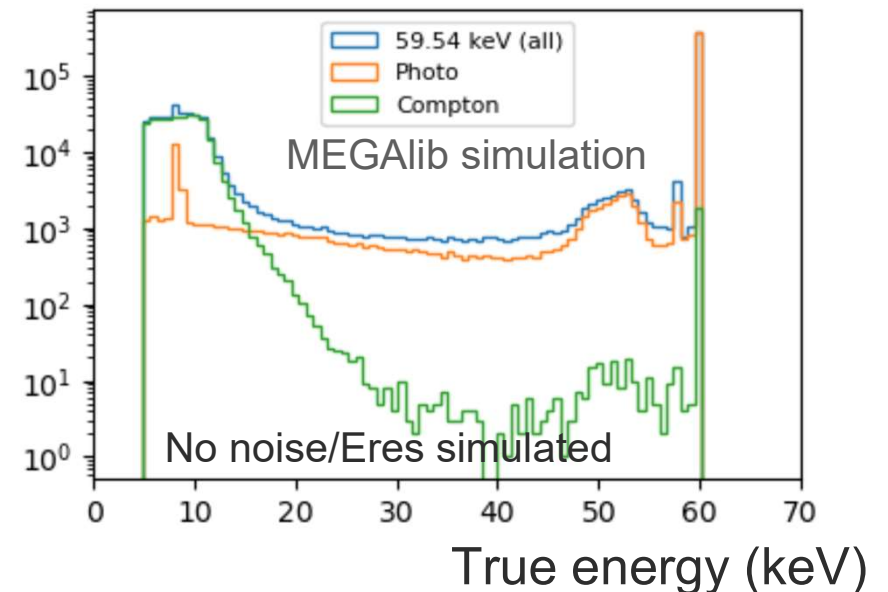
$V_{\text{built-in}}$  : Built-in potencial



Measured  $^{241}\text{Am}$  spectra



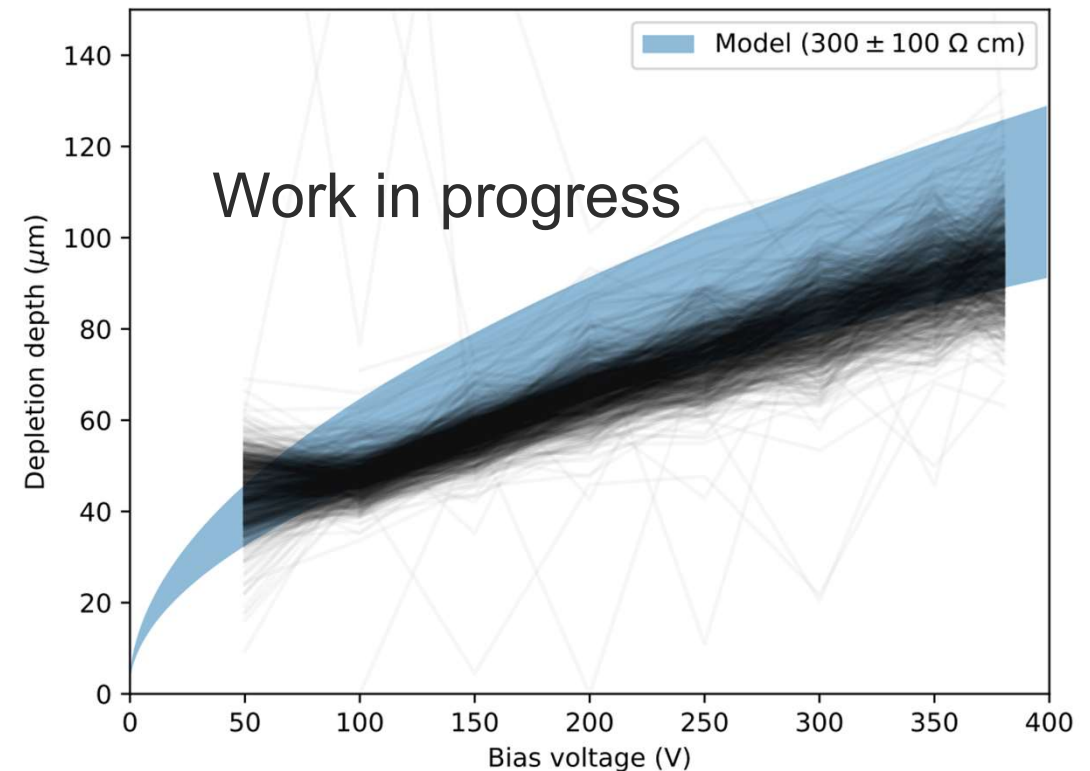
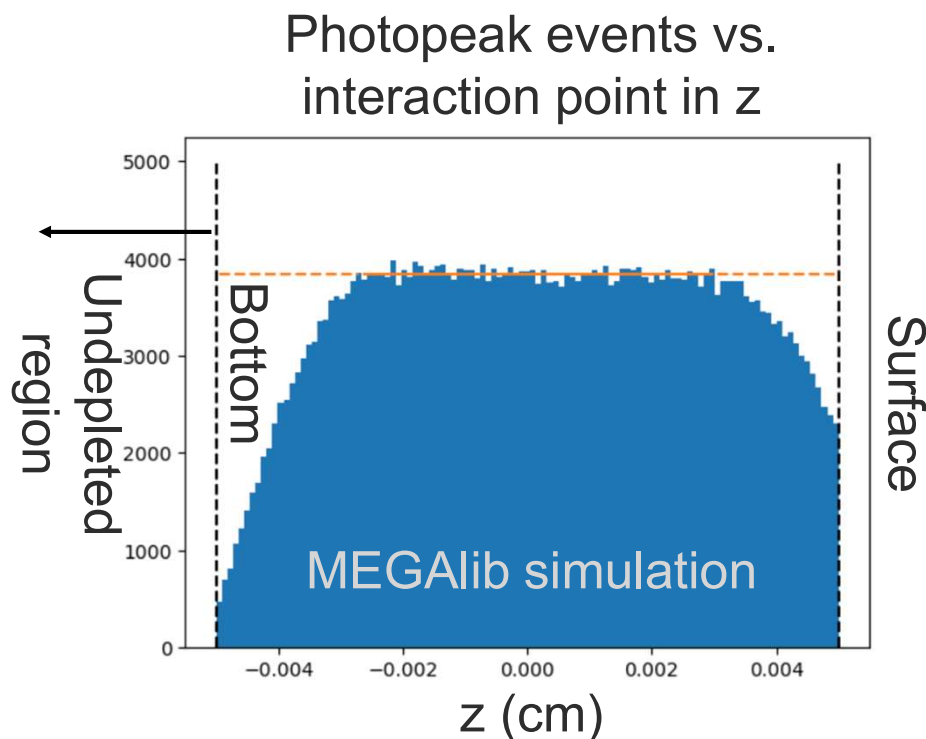
Simulated  $^{241}\text{Am}$  spectra



# V3: Depletion Depth Measurements

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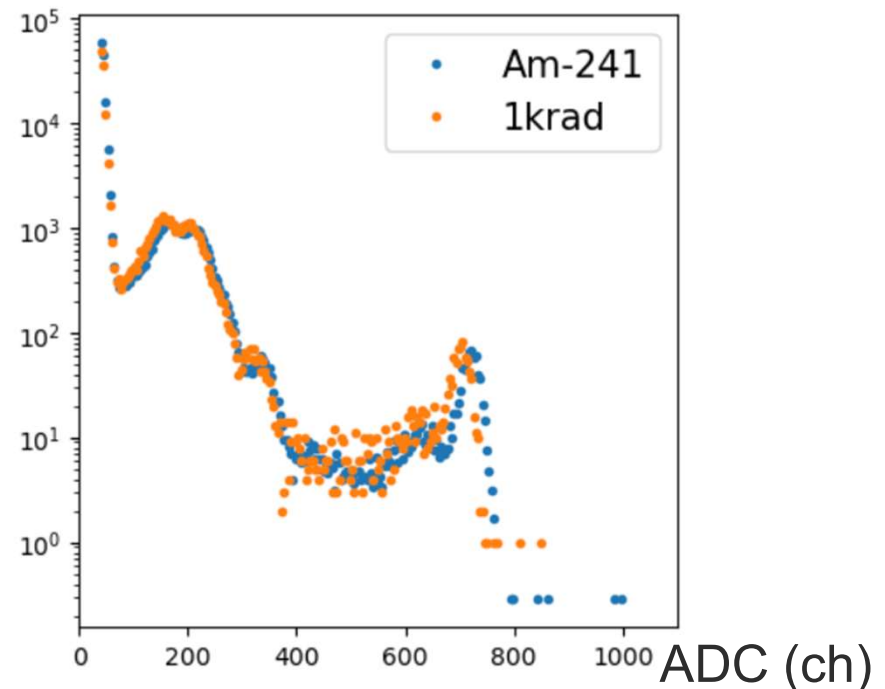
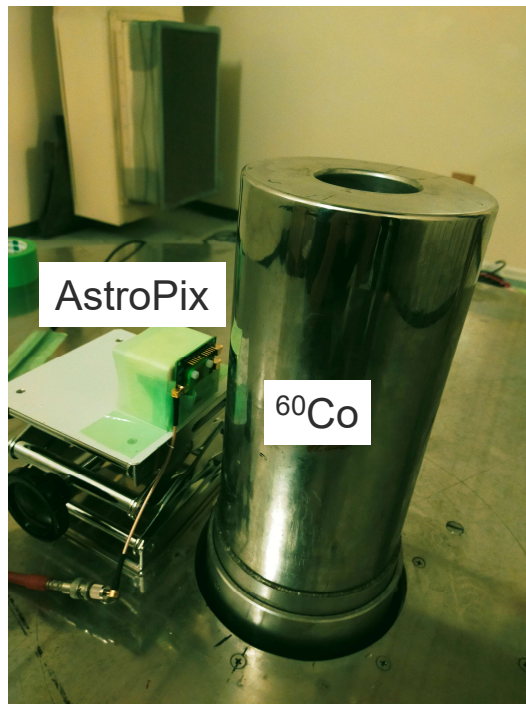
- Sensor volume is corrected for photopeak events
- Measured depths follows the model curve
- Depletion layer develops as expected, but need higher resistivity chips for full depletion



# V2: Radiation Tolerance

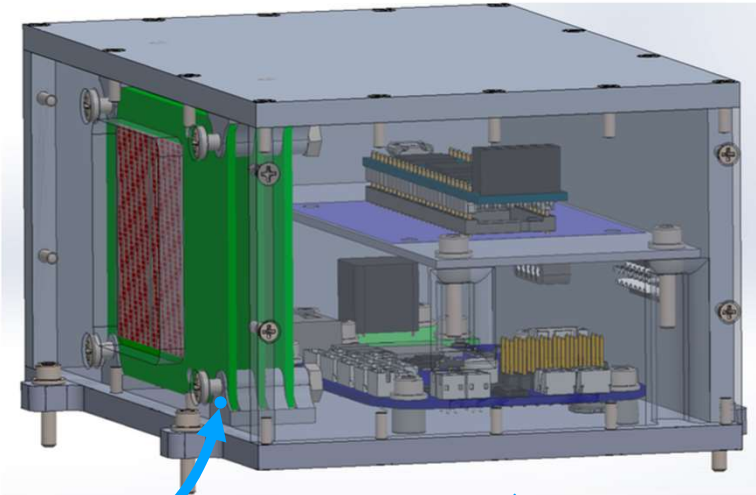
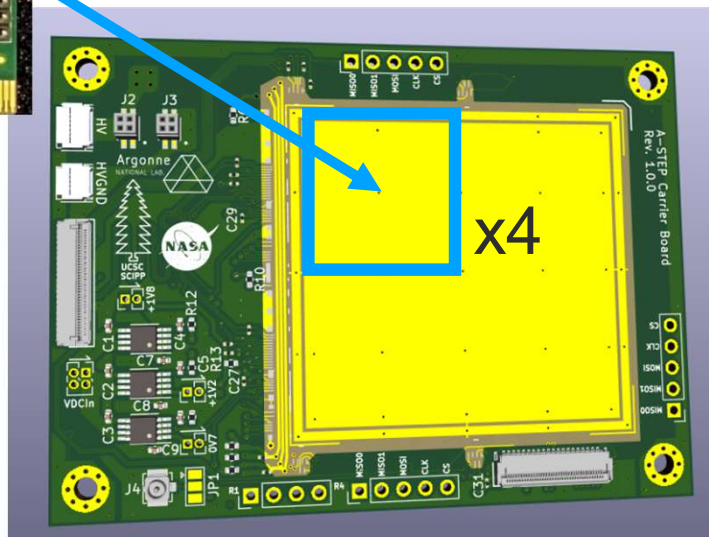
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- Expect  $\sim 1$  krad/yr irradiation on orbit
- One of V2 chips was tested at the high intensity  $^{60}\text{Co}$  facility, Hiroshima U.
- After  $\sim 1$  krad (assumed. TBC) irradiation, the chip works normally, but current draw increases by  $\sim 60\%$  and slight change in gain
- Plan to test with V3 as well

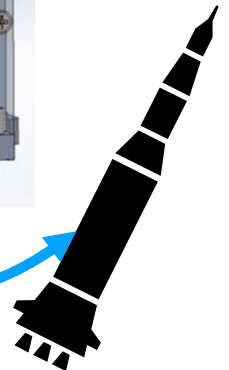


# Quad-Chip

- Minimum component of the AMEGO-X's tracker
- Quad-chip consists of 4 identical V3 chips diced together. Testing will start soon
- Sounding rocket hosted flight ("A-STEP") is planned in summer 2025 to increase Technical Readiness Level



x3 layers

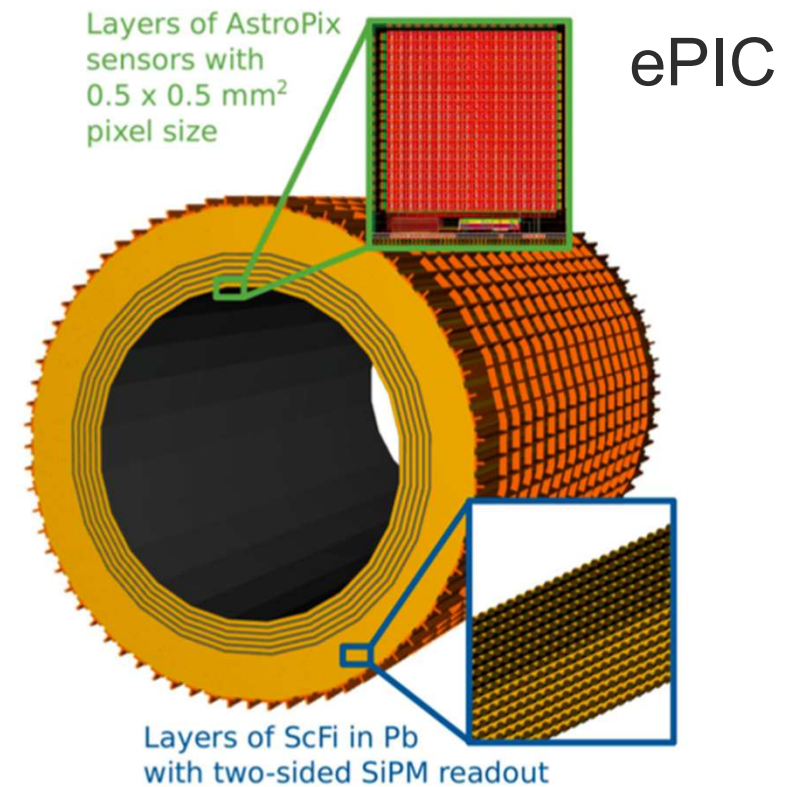
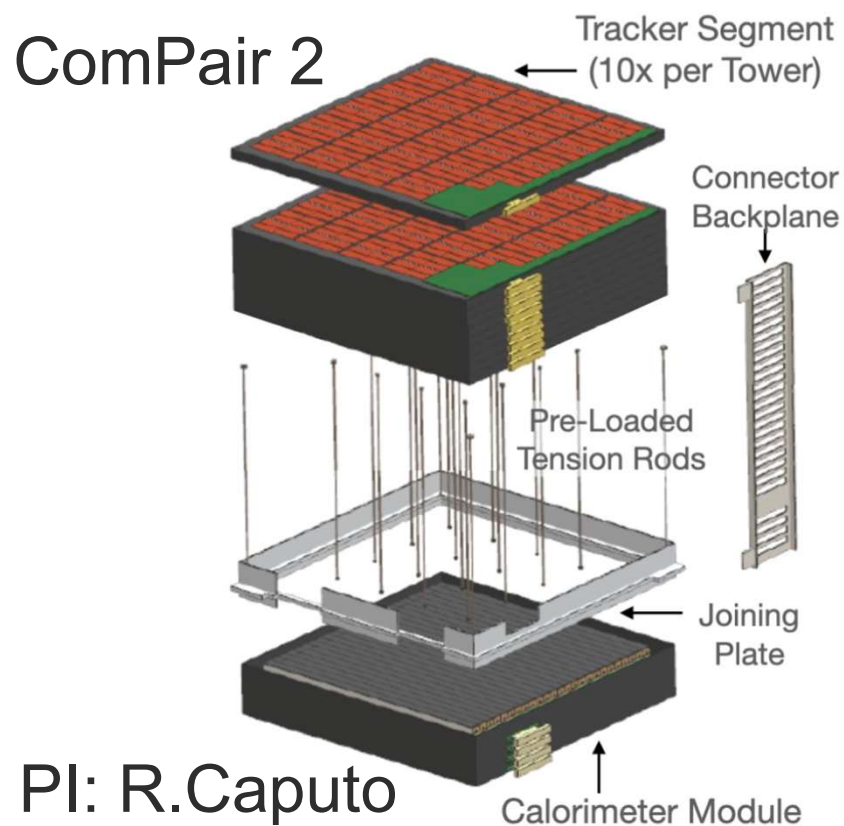


A.L.Steinhebel+ 2023

- Asynchronous timestamp clocks to reduce power consumption
  - Previous: 200 MHz clock  $\rightarrow$  TDC + 20 MHz clock generated by PLL from 2.5 MHz reference clock
- Improved time of arrival resolution: 400 ns  $\rightarrow$  3.125 ns
- Individual tune-DACs for individual pixel calibration/threshold setting
- Lower gain to expand dynamic range
- Will be available to test in this year

[N.Striebig+ 2023](#)

- ComPair 2: Compton-Pair telescope prototype
  - Prototype of AMEGO-X's tracker. Instrument integration in 2026
- ePIC detector in Electron Ion Collider (EIC)
  - Imaging electromagnetic calorimeter. AstroPix delivery in 2029



- AstroPix is a new HV-CMOS active pixel sensor being developed for future gamma-ray telescopes on board
- Pixel pitch in V3 reached to the target size
- Dynamic range is limited to 80 keV. Need to lower gain
- Full energy calibration and full imaging are possible in V3
- Measured depletion depth in V3 shows the depletion layer expands as expected, but only  $\sim 90$   $\mu\text{m}$ . Need high- $\rho$  wafer chips designed to lower leakage current
- V3 quad-chips and V4 chips are coming soon